

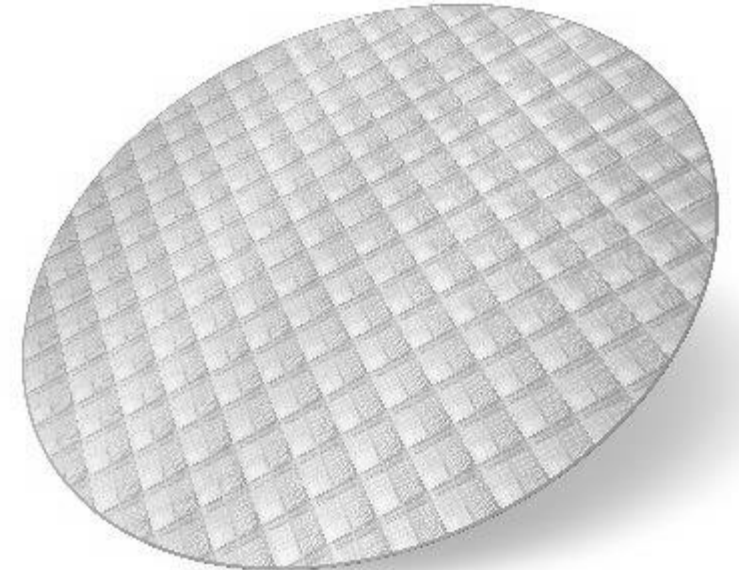
Opening Symposium; The International innovation hub in Tsukuba toward the advanced semiconductor manufacturing

Panel Discussion

28-Nov, 2021

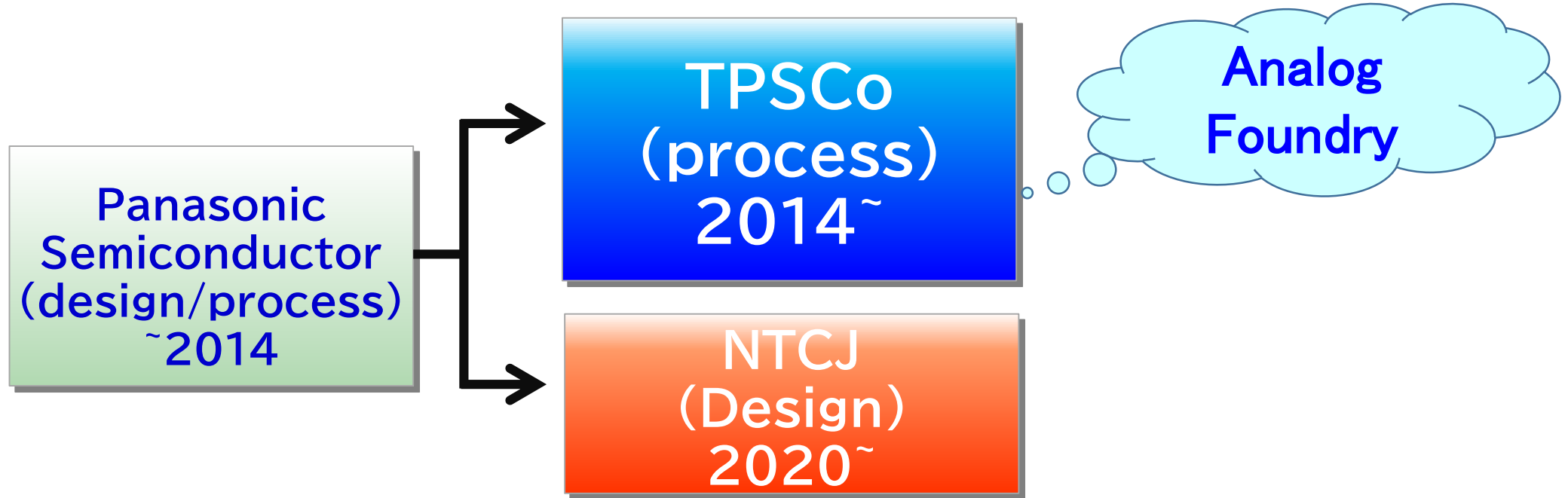
Agenda

- TPSCo Introduction
- Relationship
between TPSCo and AIST
- Expectation to Development of
Advanced Semiconductor
Manufacturing technology at
AIST



Tower Partners Semiconductor[TPSCo]

- Former Panasonic Semiconductor became TPSCo[2014~] supported by Tower Semiconductor[2014~] and Panasonic[2014-2020]/NTCJ(Nuvoton)[2020~]



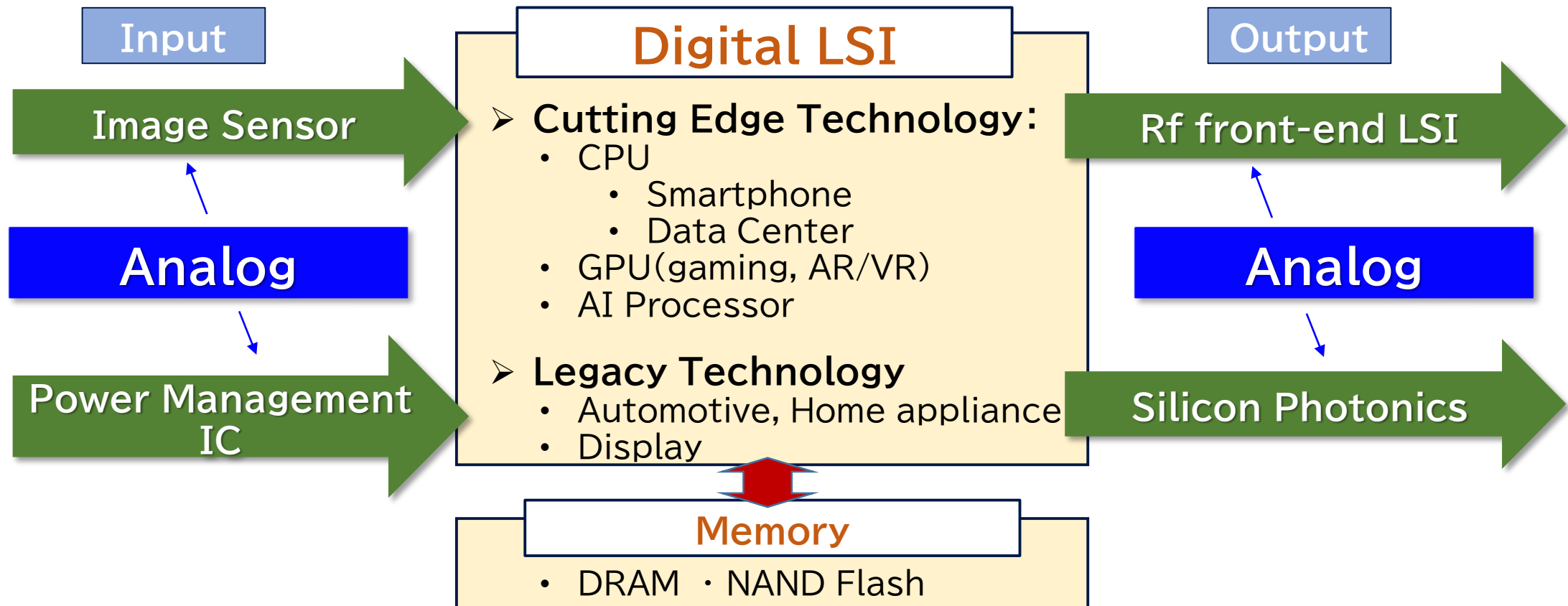
Vision : Lead the analog semiconductor renaissance in Japan,

“Because life is analog.”

- All information we acknowledge is analog, and TPSCo will support it
- TPSCo will become semiconductor renaissance in Japan

Analog and Digital Semiconductor

- Analog is “Real World”
- For Digital Transformation(DX), we need both Analog and Digital
- TPSCo is focusing on Leading Edge Analog Technology



Relationship between TPSCo and AIST/TIA

- **TPSCo is a support member of AIST Consortium for Advanced Semiconductor Manufacturing Technology**
- **Some relationship example(Startup companies)**

Customer	Start	Overview
Customer_A	2016~	After successful development through one of the NEDO program at AIST/TIA/SCR, technology was introduced to TPSCo. Multiple products are under productization in TPSCo
Customer_B	2017~	After joint development between TPSCo and customer, customer is developing some key technologies in AIST/TIA/SCR
Customer_C	2018~	After successful development in AIST/TIA/SCR with the support from METI and NEDO, customer product is under productization in TPSCo

Expectation from TPSCo viewpoint

- TPSCo is the leading supplier of 5G-RF-frontend. We expect promising technology will be created from these activities to accelerate post 5G era.
- Chip-Stack Technology is also our focus area. Many customers require this technology. We expect to see much advanced technology from these activities to meet the future requirement from our customers.
- TPSCo or our customers may require key technologies developed through “FEOL PJ: 2nm node-nanosheet technology”
- We would like to see more advanced development not only for domestic tool suppliers or material suppliers, but also domestic semiconductor manufacturers. We expect that these projects will be the trigger for such future growth.

Thank you

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